

# **DIGITAL-ANALOG LAB**

**OMEGA TYPE DAL-1043** 

The DIGITAL-ANALOG LAB is intended for elementary as well as advance training of Digital & Analog electronics. The trainer covers regular digital & analog circuits by solder-less interconnections on breadboard and as well as compatible with all optional modules, through use of 2mm brass terminals and patch cords. Various clock generators, logic level input/output indicators and DC regulated power supplies etc. are in-built. The unit housed in attractive enclosure is supplied with mains cord, patch cords, Instruction manual and Component Set.

01 Logic gates operation

**Binary Multiplier** 

Johnson Counter

Binary to Gray code conversion

Gray code to Binary conversion

Binary Addition and Subtractor

EX-OR gate implementation

Application of EX-OR gate

Binary to Excess-3 code conversion

To verify the dual nature of Logic Gates

Study of Flip-Flops RS, JK, D&T

4 Bit Binary up and down counter

Multiplexer and Demultiplexer

Study of 8 to 3 Line Encoder

To verify De-morgan's theorem With boolean logic equations

Digital

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## **EXPERIMENTAL COVERAGE:**

### Analog

- Study of Diodes in DC circuits 01
- 02 Study of Light Emitting Diodes in DC Circuits
- 03 Study of Half wave rectifier
- 04 Study of Full wave rectifier
- Study of Zener Diode as a voltage regulator 05
- 06 Study of transistor series voltage regulator
- Study of transistor shunt voltage regulator 07
- Study of Low pass filter 08
- Study of High pass filter 09
- Study of band pass filter 10
- Study of CE configuration of NPN transistor 11
- 12 Study of CB configuration of NPN transistor
- Study of CE amplifier 13
- Study of Monostable multivibrator using transistor 14
- Study of Bistable multivibrator using transistor 15
- Study of Astable multivibrator using transistor 16

# **FEATURES:**

Bread Board

Connecting terminals

LED Bar Graph

Logic Probe

Accessories

Instruction manual

Power

Weight

3-07

Dimension

- 16 Study of 3 to 8 Line Decoder 17 Study of Shift Register (SIPO) 18 **CMOS-TTL Interfacing**
- 19 Study of Crystal oscillator 20 Study of pulse stretcher circuit Unique solder-less large size, spring loaded breadboard consisting of twoTerminal Strips with 1280 tie points and 4 Distribution Strip swith 100 tie points each, totaling to 1680 tie points. (Size:112mm x170mm) Regulated DC Power Supply +5 V at 1 Amp, -5 V at 1 Amp, +12 V/0 to 20V at 500mA, and -12 V/0 to -20 V at 500 mA 5-0-5V, 10-0-10V at 100mA. Can be used as 5V, 10V, 15V, 20V, and also as center tap AC Supply : Sine / Square / Traingular / Pulse waveform frequency 1 Hz to 110 Khz in 5 Steps. Variable in between Function Generator steps. Sine / Square / Traingular waveform output 50mV ~ 10Vpp variable **Clock Generators** : 0.1Hz and 100 Hz, Independent fixed TTL 5V outputs Variable Clock Generators low frequency variable clock 1 Hz to 10 Hz Fixed TTL 5V output : Pulser Switch 2 independent buffered bounce free manual pulser (useful for freezing the action of each stage of the counter after every clock pulse) Data Switch 16 independent logic level inputs to select High / Low TTL levels, each with a LED to indicate high / low : status and termination 16 independent buffered logic level indicators for High / Low status indication of digital outputs Logic Indicators Speaker 8 ohms miniature speaker with terminations Dual range DC Voltmeter 0-200 / Ammeter 0-200mA Digital meter (3<sup>1</sup>/<sub>2</sub>Digit) For testing the continuity. Provided with Beeper Sound **Continuity Tester** Potentiometers : 6 Potentiometers (1K, 22K, 47k, 100K, 100K and 1Meg) with terminals 2 Nos. BNC to 2 channel banana adapter BNC to banana adapter : Computer interface Facilities connecting your trainer to either Rs232 communication port of PC ADAPTER using 25 pin (male) : connector through 25 nos. of 2 mm banana sockets On Board Switches 2 Switches singal pole double through :
- 2 / 4 connecting terminals Seven segment LED Display
  - 2 Nos. BCD to Seven Segment Decoder/Driver IC with terminals :
  - With 10 LED Indicators and 20 termination :
  - Logic level indicator for TTL/CMOS :
    - 230 V ± 10%, 50 Hz :
    - :
  - Mains cord, Operating and Experimental manual, Red & Black patch cords (2mm with Pin) 10 each, Red & Black patch cord (Pin to Pin) 10 each & Component Set
  - Strongly supported by detailed operating instructions :
    - 6 Kg. (Approx)
    - W 415 x H 165 x D 315 •

### Continue....2

We are committed to the continuous development of our products, and therefore reserve the right to amend specifications without prior notice.

# OMEGA ELECTRONICS

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# MODULES FOR DIGITAL- ANALOG LAB

**OMEGA TYPE DAL-1043** 

To verify De-morgan's theorem with boolean logic equations

### **OPTIONAL MODULES:-**

These are ready to use modules with wired components & circuit schematic drawn on top compatible to use out of bellow modules can be used with our Following Training Boards. Digital Lab (DL-1041), Analog Lab (AL-1042), Digital -Analog Lab (DAL-1043), Digital Trainer (DL-1044), Logic Lab (DL-1047), Digital Lab Station (DL-1049) Bread Board Circuit Lab (BBS-105), LTB-841, LTB-842, LTB-845.



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Multiplexer and Demultiplexer

Study of 8 to 3 Line Encoder

Study of 3 to 8 Line Decoder

CMOS-TTL Interfacing

4 Bit Ring Counter

Decade counter

Study of Crystal oscillator

Study of Shift Register (SIPO)

Study of pulse stretcher circuit

Shift Register SISO and PIPO

Decimal to BCD Converter

: 0.7 Kg. (Approx)

Modulo 12 Counter By Direct Clearing

Astable Multivibrator using Digital IC

Bistable Multivibrator using Digital IC

Monostable Multivibrator using Digital IC

### A001. Study of Diode in DC circuits D001 A002. Study of Light Emitting Diodes in DC Circuits D002 A003. Study of Half wave rectifier D003 A004. Study of Full wave rectifier D004 A005. Study of Zener Diode as a voltage regulator D005 A006. Study of transistor series voltage regulator D006 A007. Study of transistor shunt voltage regulator D007 A008. Study of Low pass filter D008 A009. Study of High pass filter D009 A010. Study of band pass filter D010 A011. Study of CE configuration of NPN transistor D011 A012. Study of CB configuration of NPN transistor D012 A013. Study of CE amplifier D013 A014. Study of Monostable multivibrator using transistor D014 A015. Study of Bistable multivibrator using transistor D015 D016 A016. Study of Astable multivibrator using transistor A017. Study CB amplifier (PNP) D017 A018. Study CC amplifier (PNP) D018 A019. Study of FET amplifier. D019 A020. Study power supply having two zener diodes in series D020 A021. Study dual polarity voltage regulated power supply D021 A022. Study the characteristics of photo transistor D022 A023. To practically understood the operation D023 of a 7-segment LED display D024 A024. To Study CC configuration of NPN transistor D025 A025. Study CE configuration of PNP transistor D026 A026. Study CB configuration of PNP transistor D027 D028 A027. Study CC configuration of PNP transistor A028. Study full wave dual polarity supplies D029 Octal to binary Encoder D030 4 Bit Magnitude Comparator A029. Study of FET characteristic A030. Verify superposition theorem D031 Interface of TTL-IC to CMOS-IC & CMOS IC to TTL-IC A031. Verify the vonin's theorem D032 Digital to analog converter (using IC DAC 0808) A032. Verify receprocity theorem A033. Study of Phase shift audio oscillator (Solid State) Weight Dimension : W 176 x H 131 x D 37 A034. Verify kirchoff's law (V&I) A035. Verify ohm's law A036. Ideal resistance characteristics A037. Verification of series law of resistance

- A038. Verification of parallel law of resistance
- A039. Verify maximum power transfer theorem
- A040. Study of series and parallel resistance, capacitors and inductance circuits
- A041. Study of basic electrical DC circuits
- A042. Study of AC circuits
- A043. Study of series and parallel resonance and operational
- amplifier circuits
- A044. Study of power supply circuit, 555 timer and solid state switch
- A045. Study of difference Amplifier
- A046. Analog to digital converter (using IC ADC 0800)

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