

INTERFACING CMOS TO TTL & TTL TO CMOS IC'S

OMEGATYPE LTB-865



OMEGA TYPE LTB-865 Computer Logic Training Board has been designed specifically for the study of Interfacing CMOS to TTL and TTL to CMOS IC's.

The board is absolutely self contained and requires no other apparatus. Practical experience on this board carries great educative value for Science and Engineering Students.

OBJECT

- 01 To study interfacing of TTL IC to CMOS IC.
- 02 To study interfacing of CMOS IC to TTL IC.

FEATURES

The board consists of the following built in parts:

- 01 5V D.C. at 30mAIC regulated Power Supply.
- 02 12V D.C. at 30mAIC regulated Power Supply.
- 03 0-12V D.C. At 30mA continuously variable regulated Power Supply.
- 04 Quad, 2-input NAND gate TTLIC.
- 05 Quad, 2-input NAND gate (open collector) TTLIC.
- 06 Quad, 2-input NAND gate CMOS IC.
- 07 Transistor and adequate no. of other electronic components.
- 08 Main ON/OFF switch, fuse and jewel light.
- 09 Unit is operative on 230±10% at 50Hz ac mains.
- 10 Adequate nos. of patch cords stackable 4mm spring loaded plug, length 50cm.
- 11 Good quality, reliable terminals/Sockets are provided at appropriate places on panel for connections/observations.
- 12 Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.
- 13 Weight : 2.200 Kg. (Approx).

14 Dimension : W 340 x H125 x D 210

We are committed to the continuous development of our products, and therefore reserve the right to amend specifications without prior notice.

OMEGA ELECTRONICS